

WEST Search History

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DATE: Thursday, January 12, 2006

<u>Hide?</u>	<u>Set Name</u>	<u>Query</u>	<u>Hit Count</u>
		<i>DB=PGPB,USPT; PLUR=YES; OP=OR</i>	
<input type="checkbox"/>	L46	L44 and librar\$	3
<input type="checkbox"/>	L45	L44 and l17	1
<input type="checkbox"/>	L44	L34 and (low near3 voltage)	232
<input type="checkbox"/>	L43	L42 and l18	24
<input type="checkbox"/>	L42	librar\$ with alternate	845
<input type="checkbox"/>	L41	("low voltage threshold" or lvt) same (nwell or "n-well")	21
<input type="checkbox"/>	L40	L35 and (nwell or "n-well")	18
<input type="checkbox"/>	L39	((add or adding) with (mask or shape)) same (over with (nwell or "n-well"))	1
<input type="checkbox"/>	L38	((("p-channel" same ("field effect transistor" or fet)) or pfet or "p-fet") and l36	1
<input type="checkbox"/>	L37	L36 and (nwell or "n-well")	4
<input type="checkbox"/>	L36	(add or adding) with ("low voltage threshold" or lvt)	36
<input type="checkbox"/>	L35	L34 and l17	94
<input type="checkbox"/>	L34	convert\$ with ((("p-channel" same ("field effect transistor" or fet)) or pfet or "p-fet")	365
<input type="checkbox"/>	L33	l25 and l16	13
<input type="checkbox"/>	L32	L31 not l26	144
<input type="checkbox"/>	L31	l25 and (replac\$ or substitut\$)	165
<input type="checkbox"/>	L30	l25 and librar\$	7
<input type="checkbox"/>	L29	l26 and librar\$	1
<input type="checkbox"/>	L28	L17 and l25	1
<input type="checkbox"/>	L27	L26 amd librar\$	144211
<input type="checkbox"/>	L26	((("p-channel" same ("field effect transistor" or fet)) or pfet or "p-fet") same (low near3 voltage)) and (((("n-channel" same ("field effect transistor" or fet)) or nfet or "n-fet") same (standard near3 voltage))	55
<input type="checkbox"/>	L25	((("p-channel" same ("field effect transistor" or fet)) or pfet or "p-fet") and (low near3 voltage)) and (((("n-channel" same ("field effect transistor" or fet)) or nfet or "n-fet") and (standard near3 voltage)))	278
<input type="checkbox"/>	L24	"alternate voltage threshold"	1
<input type="checkbox"/>	L23	5666288[uref]	54
<input type="checkbox"/>	L22	6668358[uref]	2
<input type="checkbox"/>	L21	l9 and l18	6

DB=USPT,PGPB; PLUR=YES; OP=OR

(4613940| '4813013| '5084824| '5258919| '5274568| '5406497| '5452227|

L20 *'5459673| '5487018| '5493508| '5510998| '5519627| '5572436| '5572437)! [pn]* 14

DB=PGPB,USPT; PLUR=YES; OP=OR

L19 L18 and 117 12

L18 716/\${ccls} 9356

L17 librar\$ with hybrid 3965

DB=USPT,PGPB; PLUR=YES; OP=OR

L16 *('5970018| '6038386| '6111427| '6407945| '6426261)! [pn]* 5

DB=PGPB,USPT; PLUR=YES; OP=OR

L15 L13 and librar\$ 23

L14 L13 and L9 3

L13 L12 and L11 812

L12 enhanc\$ with performance 87154

L11 (reduc\$ with "leakage current") 13302

L10 L9 and librar\$ 7

L9 L8 and L6 115

L8 L7 2030

L7 (low\$ near5 voltage) same (("p-channel" same ("field effect transistor" or fet) or pfet or "p-fet") 2030

L6 replac\$ with (("p-channel" same ("field effect transistor" or fet)) or pfet or "p-fet") 516

L5 replac\$ with ("p-channel" or pfet or "p-fet") 2054

L4 L3 and librar\$ 2

L3 L2 and L1 7

L2 "low voltage threshold" or lvt 2478

L1 "standard voltage threshold" or svt 762

END OF SEARCH HISTORY